

Symbolic Approach for Side-Channel Resistance Analysis of Masked Assembly Codes

Workshop PROOFS

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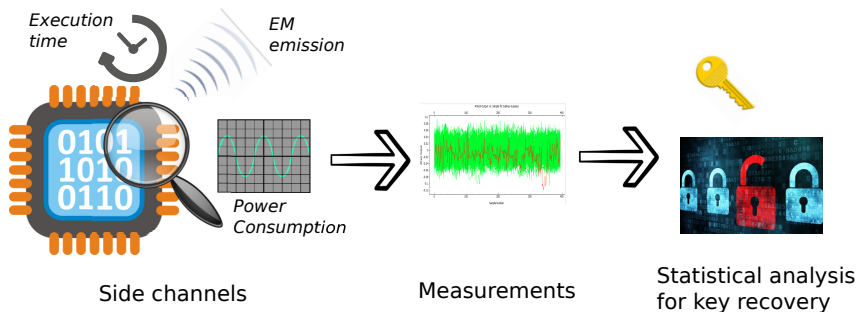
1 Introduction / Motivation

2 Symbolic Method

3 Experiments

4 Conclusion

Side-Channel Attacks



The Masking Countermeasure

Aim: observation of d intermediate computations cannot reveal the secret $x \implies d$ -th order masking

- Splits a secret x in $d+1$ shares using random uniform variables called *masks*
- Operation-dependent, i.e boolean masking: $x \oplus m$
- At software level, usually added in the source code (easy to identify secret variables)

Problems

- Need to ensure that a masked program is leakage free in practice
- Compilation flow and optimizations (reordering, removal...) may affect masking effectiveness

Masked Programs Security: Existing Formal Verifications

- [Bayrak, CHES13] SAT verification of *sensitivity*: an operation on a secret must involve a random variable which is not a *don't care* variable (i.e it affects the result)
 - ✓ Low level: LLVM programs
 - ✗ Security property not sufficient
- [Eldib, TACAS14] SMT verification of *perfect masking*, i.e statistical independency of intermediate computations from secrets
 - ✓ Strong security property
 - ✗ C level & Bit-blasted programs (could be applied to low level)
 - ✗ Lack of scalability (combinatorial blow-up of the enumeration)
- [Barthe, Eurocrypt15] *t-non-interference*: joint probability distribution of any t intermediate expressions is independent from secrets
 - ✓ Strong security property
 - ✓ Good scalability
 - ✗ Cannot conclude for some cases

Our Goal

To verify side channel resistance:

- Of 1st order **masked** programs
- At **assembly** level
- In the **value-based model**: instruction result leaks
- Considering that: leakage-free instruction \iff result is **statistically independent** from secrets
- With a **symbolic approach** that infers the distribution type of instruction expressions

Plan

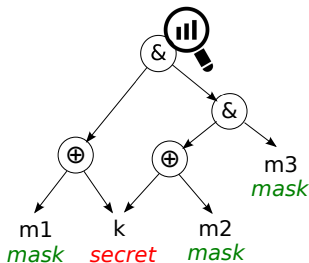
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Verification Scheme

```

# r0 ← k; r1 ← m1; r2 ← m2; r3 ← m3
1 eor r4, r0, r1 # k ⊕ m1
2 eor r5, r0, r2 # k ⊕ m2
3 and r5, r5, r3 # (k ⊕ m2) & m3
4 and r5, r5, r4 # (k ⊕ m1) & ((k ⊕ m2) & m3)

```



Data dependency graph of the last instruction

Is the root distribution statistically independent from k ?

- ▶ Inputs tagged with a distribution type
- ▶ Bottom-up combination of distribution types using defined inference rules

Symbolic Approach

4 distribution types for variables and expressions:

- Random Uniform Distribution (**RUD**)
- Unknown Distribution (**UKD**)
- Constant (**CST**)
- (Statistically) Independent from Secrets Distribution (**ISD**): not necessarily uniform but identical for all values of the secrets.

k: secret

m_1, m_2 : masks

$e = (k \oplus m_1) \& m_2$

$e' = (k \oplus m_1) \& m_1$

k	m_1	m_2	e
0	0	0	0
	0	1	0
	1	0	0
	1	1	1
1	0	0	0
	0	1	1
	1	0	0
	1	1	0

$$\left. \begin{array}{l} P(e=0) = \frac{3}{4} \\ P(e=1) = \frac{1}{4} \end{array} \right\}$$

$$\left. \begin{array}{l} P(e=0) = \frac{3}{4} \\ P(e=1) = \frac{1}{4} \end{array} \right\}$$

e'
0
0
1
1
0
0
0
0

$$\left. \begin{array}{l} P(e'=0) = \frac{1}{2} \\ P(e'=1) = \frac{1}{2} \end{array} \right\}$$

$$\left. \begin{array}{l} P(e'=0) = 1 \\ P(e'=1) = 0 \end{array} \right\}$$

Independence Notions

Which distribution types assert that an expression is statistically independent from secrets?

Dependence between expression e and variable v :

- *structural* $\implies v$ appears in e
- *statistical* \implies the distribution of the result of e depends on v

\implies Need to keep track of structural dependencies: $(k \oplus m) \& m$

Safe types:

- $e \sim \text{RUD}$
- $e \sim \text{ISD}$
- $e \sim \text{UKD}$ with no structural dependency on any secret

Unsafe type:

- $e \sim \text{UKD}\{\text{dep}\}$ with structural dependency on some secret variable: $\text{dep} \cap S \neq \emptyset$

Dominant Masks

Aim: to find a mask that randomizes the whole expression

Dom Rule

- expression $e = e' \oplus m$ or $e = e' + m \bmod 2^n$
- $m \sim \text{RUD}\{m\}$
- $m \notin \text{dep}(e')$

$\implies e \sim \text{RUD}$ and m is a **dominant mask** of e .

2 sets of dominant masks:

- $\text{dom}_{\oplus}(e)$ the set of xor dominant masks of e
- $\text{dom}_{+}(e)$ the set of additive dominant masks of e

Examples:

- $\text{dom}_{\oplus}((k + m1) \oplus (k \oplus m1 \oplus m2)) = m2$
- $\text{dom}_{+}((k + m1) \oplus 0) = \text{dom}_{+}(k + m1) = m1$

Other Inference Rules

By distribution types:

- Set of rules for \oplus , $+$ mod 2^n
- Set of rules for AND and OR

Disjoint rule for binary operators

- $u \sim \text{ISD}\{\text{dep0}\}$ and $v \sim \text{ISD}\{\text{dep1}\}$
- No masks in common: $\text{dep0} \cap \text{dep1} \cap M = \emptyset$

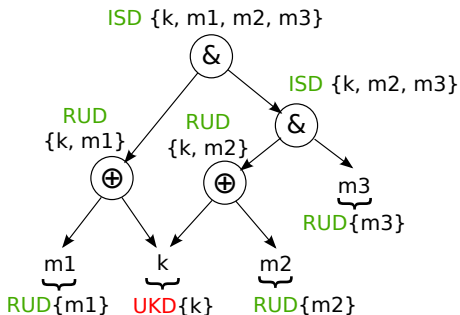
$\implies (u \text{ op } v) \sim \text{ISD}\{\text{dep0} \cup \text{dep1}\}$ for every binary operation op

▷ More details in the paper

Running Example

Type inference for the last instruction $i4$:

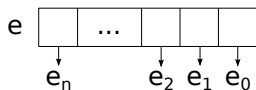
$(k \oplus m_1) \& ((k \oplus m_2) \& m_3)$



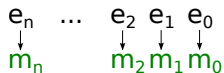
▷ $i4$ is statistically independent from k

Bit Level Analysis

When no conclusion is possible at word level:
 \implies split the expression into several expressions at bit level

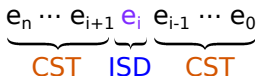


▷ case 1:



$e_j \sim$ RUD and different dominant mask for each e_j

▷ case 2:



Concatenation of an ISD bit with CST bits

▷ case 3:



Deduplicated ISD bit and concatenation with CST bits

Example from mix columns in AES:

$$e = ((\text{LSR}(\text{mt1} \oplus \text{mp} \oplus \text{sbox5}, 7) \oplus \text{LSR}(\text{mt2} \oplus \text{mp} \oplus \text{sbox10}, 7)) +$$

$$(((\text{LSR}(\text{mt1} \oplus \text{mp} \oplus \text{sbox5}, 7) \oplus \text{LSR}(\text{mt2} \oplus \text{mp} \oplus \text{sbox10}, 7)) \ll 1))$$

$$b_7 = \text{mt1}_7 \oplus \text{mp}_7 \oplus \text{sbox5}_7 \oplus \text{mt2}_7 \oplus \text{mp}_7 \oplus \text{sbox10}_7$$

$$e \implies 0000 \ 00b_7b_7 \implies \text{ISD}$$

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Comparison with Two Methods

- **Our method**: distribution type inference implemented in Python
- **C-enumerative**: generates a C program that computes the expression distribution by enumerating on all variable values
 - ▶ returns: RUD, ISD or vulnerable
- **SMT-enumerative**: extends Eldib *et al.*'s approach for n -bit variables (generates a SMT problem that searches for two values of a secret for which the expression distribution is different)
 - ▶ returns: ISD or vulnerable

Benchmarks

Program	#ASM inst	Size in bits	# masks	# secrets	Secure in literature
Boolean programs for comparison with SMT					
P6 [Eldib, TACAS14]	8	1	3	3	×
Masked Chi [Eldib, TACAS14]	8	1	2	3	✓
Algorithms for switching between boolean and arithmetic maskings					
Goubin Conversion [Goubin01]	8	4	2	1	✓
Coron Conversion [Coron15]	37	4	3	1	✓
Cryptographic algorithms					
Masked AES 1st round [Herbst06]	422	8	6	16 + 16	✓
Simon TI 1st round [Shahverdi17]	15	32	5	3 + 2	✓

Experimental Comparison

Program	Ref (enumeration)			Symbolic			
	# RUD	# ISD	# Vuln	# RUD	# ISD	# UKD	# CST
P6	6	2	0	6	2	0	0
Masked Chi	2	2	4	2	2	4	0
Goubin Conversion	7	1	0	5	0	3	0
Coron Conversion	19	11	7	14	10	13	0
Masked AES 1st round	-	-	-	302	0	0	120
Simon TI 1st round	-	-	-	7	4	3	1

- Enumeration methods \implies sound, complete but not applicable on AES/Simon
- Symbolic method \implies sound $\{\text{Vuln}\} \subseteq \{\text{UKD}\}$ but not complete

Verification Time

Program	Symbolic time	Enum C time	SMT time
P6	<1s	<1s	<1s
Masked Chi	<1s	<1s	<1s
Goubin Conversion	<1s	<1s	35mn
Coron Conversion	2s	1s	5,6h
Masked AES 1st round	22s	-	-
Simon TI 1st round	8.5s	-	-

- C-enumeration \implies fast but only for small programs
- SMT-enumeration \implies can be long even for small programs
- Symbolic method \implies better scalability

Bit Level vs. Word Level Analysis

Program	#UKD _w	#UKD _b	#total inst
P6	0	0	8
Masked Chi	4	4	8
Goubin Conversion	3	3	8
Coron Conversion	21	13	37
Masked AES 1st round	80	0	422
Simon 1st round	7	4	15

With bit level analysis:

- For Coron Conversion & Simon TI: around 40% of unsafe instructions become safe
- For Masked AES: ALL unsafe instructions become safe

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Conclusion

We proposed a symbolic method:

- For verifying side channel robustness of 1st order masked programs at assembly level
- Using type inference of expression distributions
- Scalable, sound but not complete

Perspectives for future work:

- Automatic tool that analyses an assembly code
- Refine the set of rules / bit level analysis
- Combine with enumerative approach at bit level (need to consider inter-bit dependencies)
- Extend to other leakage models (e.g transition-based model) / higher masking orders

References

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- [Shahverdi17] Aria Shahverdi, Mostafa Taha, and Thomas Eisenbarth. Lightweight side channel resistance. Threshold implementations of simon. *IEEE Transactions on Computers*, 66(4):661-671, 2017.

Thank you for your attention!

Algorithm 1 Distribution inference algorithm

```
1: function INFER(E)
2:   if e is a leaf then
3:     if  $e \in S$  then return UKD{e}
4:     else if  $e \in M$  then return RUD{e}
5:     else return CST
6:   else
7:      $le\{ld\} = \text{infer}(e.\text{left\_child})$ 
8:      $re\{rd\} = \text{infer}(e.\text{right\_child})$ 
9:     if  $\exists$  rule for ( $le\{ld\}$  e.op  $re\{rd\}$ ) that returns RUD{dep}
10:    then
11:      return RUD{dep}
12:    else if  $\exists$  rule for ( $le\{ld\}$  e.op  $re\{rd\}$ ) that returns
13:    ISD{dep} then
14:      return ISD{dep}
15:    else return UKD{dep}
```
